

FIG. 1

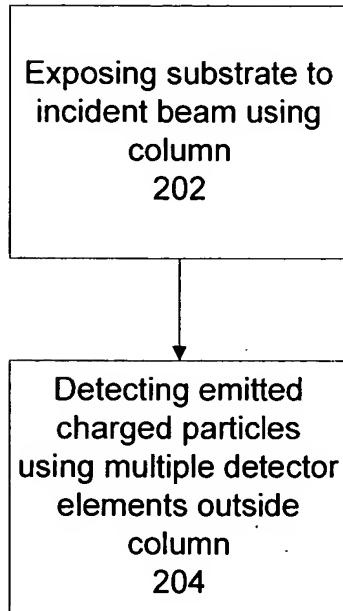


FIG. 2

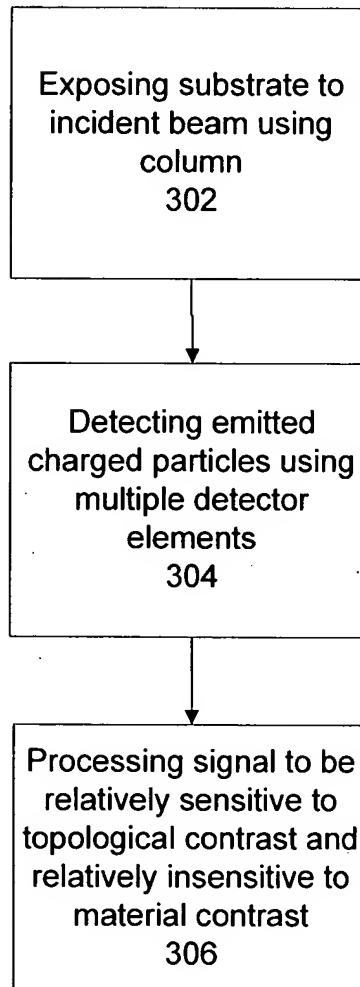


FIG. 3

*High-Speed Inspection of Flat Substrate with Underlying  
Visible Topology*

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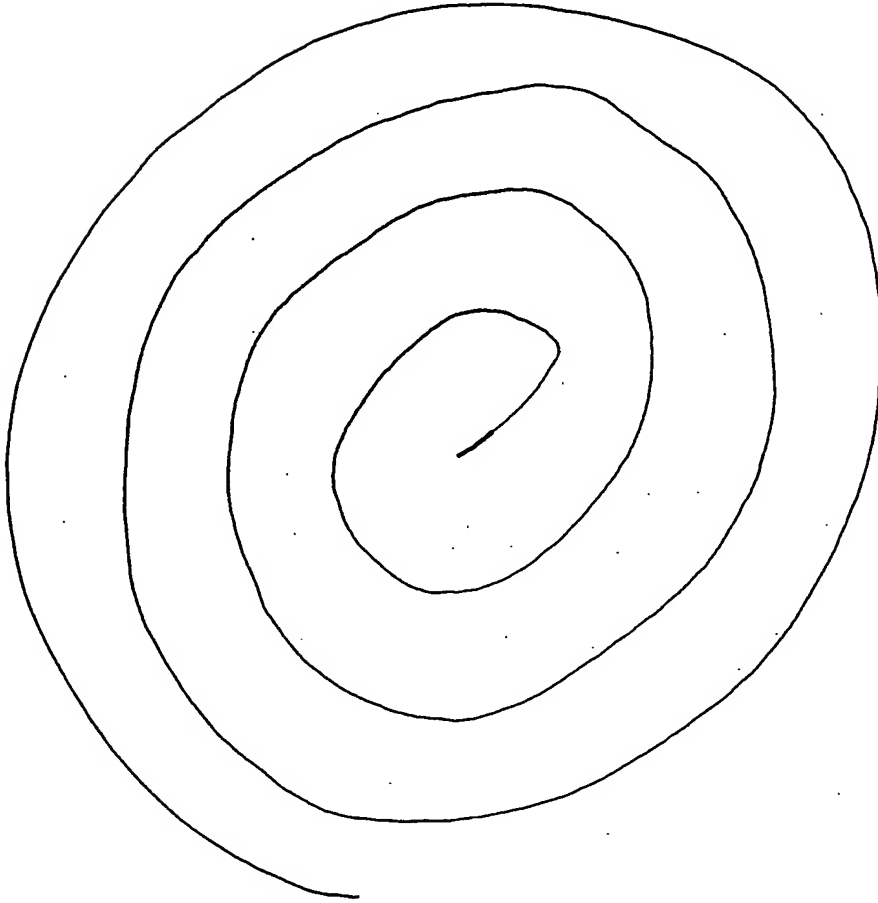


FIG. 4

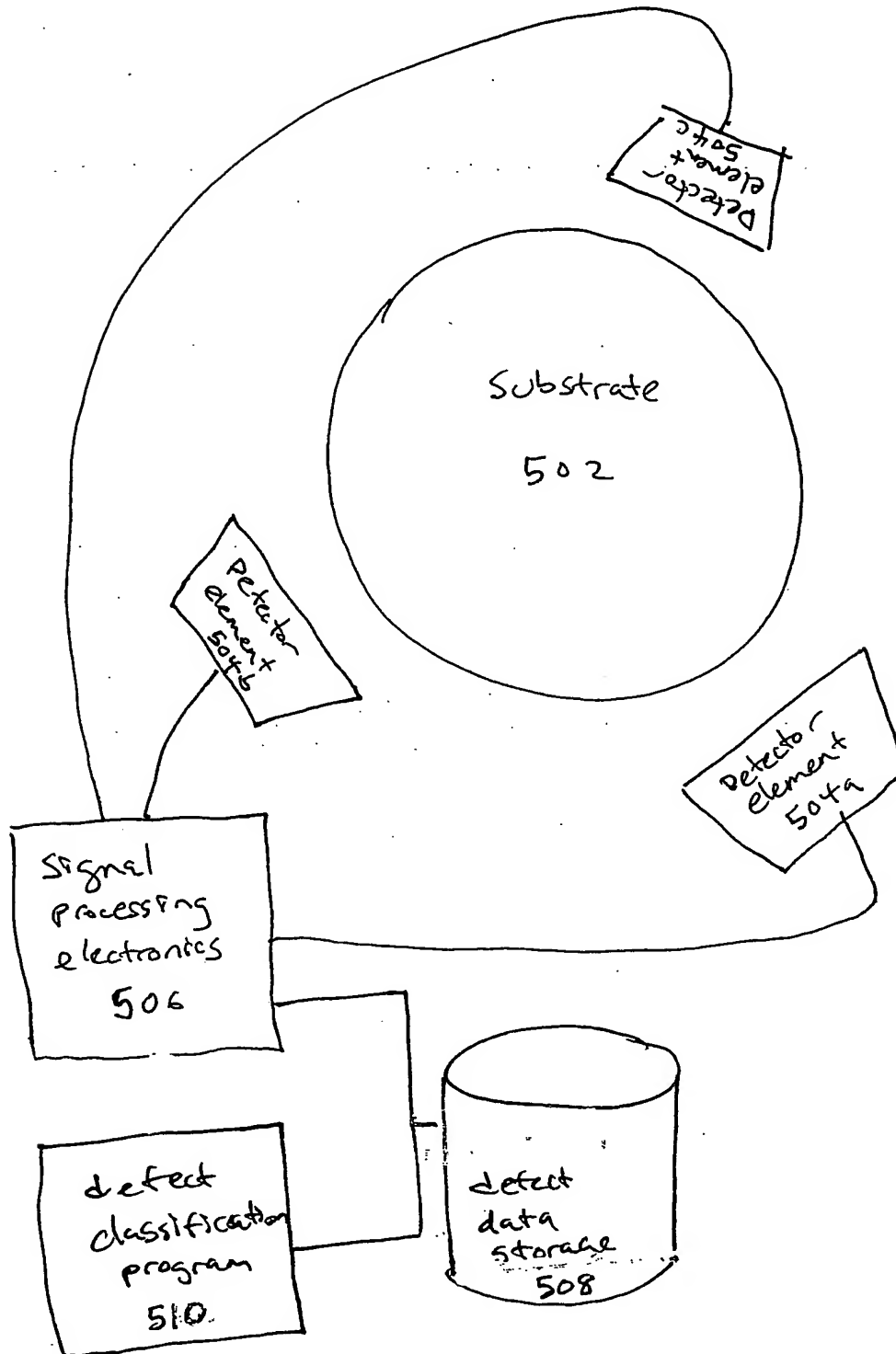


FIG. 5